

System HD IQ Module Adapter

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| FOR OPERATING INFORMATION PLEASE REFER TO THE RELEVANT MODULAR IQ OPERATION MANUAL | |

Description

System HD IQ Module Adapter

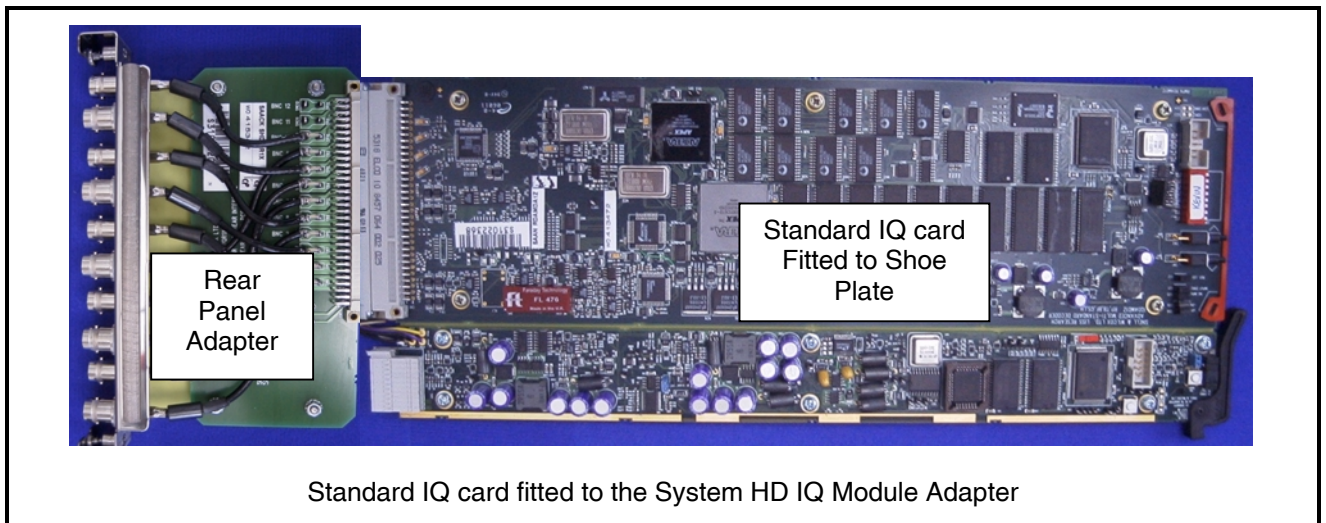
There are a wide range of Standard Definition video and audio modules in the S&W IQ Modular range and some of these modules are natural partners to the some of the System HD Modules.

A range of compatible rear panels are available.

A typical application might be a SHDUPCN Upconverter paired with an IQDMSDA Composite decoder.

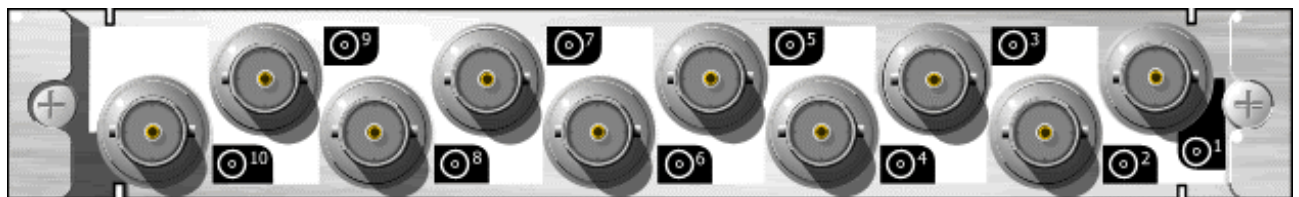
The System HD IQ Module Adapter allows IQ modules to utilise the advanced power management system and monitoring of the System HD infrastructure.

..



ADAPTER REAR PANELS

10 way BNC Adapter



7 way BNC + 26 way high density 'D' connector



Introduction to this Installation Manual

This Installation Manual is for use by qualified Service Personnel only. System HD Modules should only be installed and/or replaced by qualified personnel.

The installation and/or removal of System HD Modules should only be undertaken once the after Module Installation and Removal section of the enclosure has been read and understood.

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Features

- RollCall™ operation
- Provides data for system infrastructure monitoring
- All Voltage Rails
- BoardTemperature

Note:

RollCall™ enabled for remote system control & monitoring.

Technical Profile

INDICATOR LEDS

Standard

| | |
|-------|----------------------|
| Power | Power supplies valid |
| Fault | Board fault |
| CPU | Valid CPU activity |

RollCall™

| | |
|----------------------|-----------------------|
| RollCall monitoring: | General Alarm |
| | Supply voltage levels |
| | Board temperature |

POWER CODE

2

WEIGHT

<750gm (Main Board plus Interface Board)

Module Installation and Removal



NOTE ...

- 1 **System HD Modules must only be installed or removed by approved engineers.**
- 2 **The installation and removal of System HD Modules should only be undertaken once the Module Installation and Removal section of the appropriate enclosure installation manual has been read and understood.**

The installation of the actual module into an enclosure consists of four stages

- 1) Selection of the Slot for the module
- 2) Installation of the Rear Interface.
- 3) Installation of the Main Board.
- 4) Rear Panel Connections.

General Notes

Enclosure Power Status

The design of the System HD modules and enclosures is such as to allow live insertion in most cases. Live insertion is defined as the process of installing or replacing a module within an enclosure that is powered up.

It is very important to note that there may be factors to do with the location and other operating conditions of the enclosure which preclude the safe live insertion of modules.

Another aspect of the power status of the enclosure is ensuring that there is sufficient power available for the module about to be installed.

There is a simple calculation required to ensure that the installation of an additional module does not overload the enclosure. This calculation is fully described in the installation manual for the enclosure.

If there is a Control and Monitor board fitted within the enclosure then the Active Power Management System (APMS) operated by that board will deal with accidental power supply overloading. The mechanism by which this is managed is fully described in the installation manual for the Control and Monitor board.

Accidental overloading without a Control and Monitor board can cause “brown outs” to the other boards within the enclosure. If this overloading is allowed to continue it may cause the boards and/or the Power Supply Assemblies to shut down.

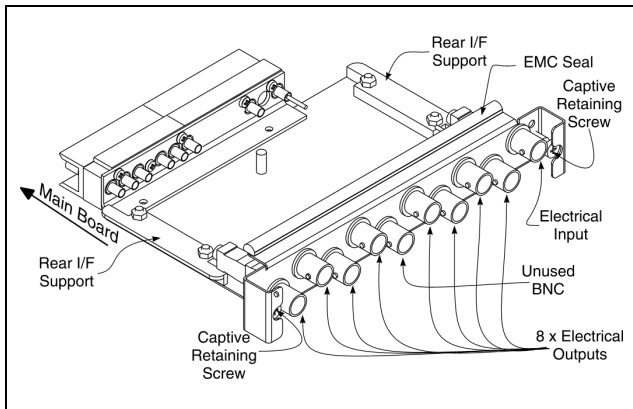
Slot Selection.

The selection of the slot into which the module is to be inserted may not be the trivial process that might be expected. Care must be taken if the enclosure into which the module is to be placed contains - or in the future may contain – a System HD Control and Monitor board. In this case the slots are prioritised with Slot 1 (top right when viewed from the front of the enclosure) being the highest priority. This is due to the action of the Active Power Management System (APMS) operating on the Control and Monitor board. A full explanation of the APMS and the prioritisation is contained in the manuals for the Control and Monitor board and the Enclosure itself.

Slot selection does not matter if there is no intention for the enclosure within which the module is to be fitted to ever incorporate a Control and Monitor board. In this case it makes no difference which slot the module is inserted into.

Installation of the Rear Interface.

The Rear Interface for the System HD Distribution Amplifier will appear very similar to the diagram below.



It will be necessary to gain access to the rear of the enclosure to fit the rear interface.

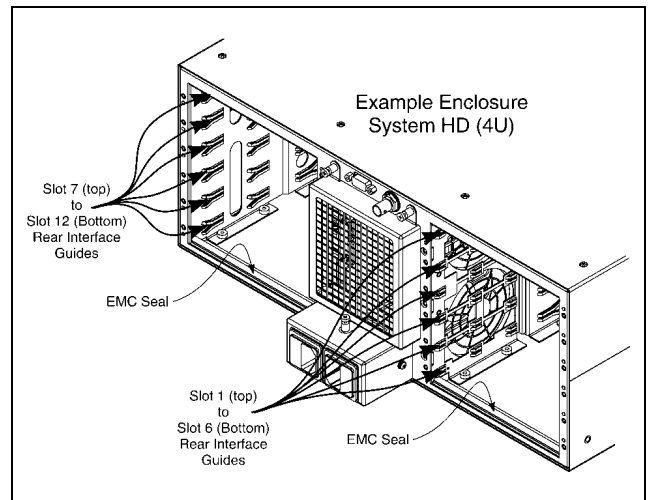
It will probably also be necessary to remove the rear blanking panel for the selected slot. This may involve moving some of the blanking panels around if the required slot is covered by a 2 or 4 slot wide blanking panel.

Ensure that there is no main board part of a System HD module fitted in the corresponding front section of the selected slot in the enclosure. Failure to ensure that the front section is empty could result in damage to the connectors between the rear interface and the main board if the two parts are not compatible. Even if the two parts are compatible the presence of the main board can make the insertion and secure location of the rear interface very difficult.

Having created a free slot space in the rear of the enclosure, and checked that the front section is unoccupied, prepare to insert the rear interface for the module.

With the rear of the selected slot exposed it will be possible to see the rear interface guides within the enclosure.

In the case of the System HD 4U Enclosure (as shown below) one side of the guides are formed as part of the case wall and the other side is integrated with the fan support metalwork. These guides are labelled on the diagram.



Locate the rear interface supports into the ends of the rear interface guides on the enclosure.

Ensure that **both** sides are correctly located.

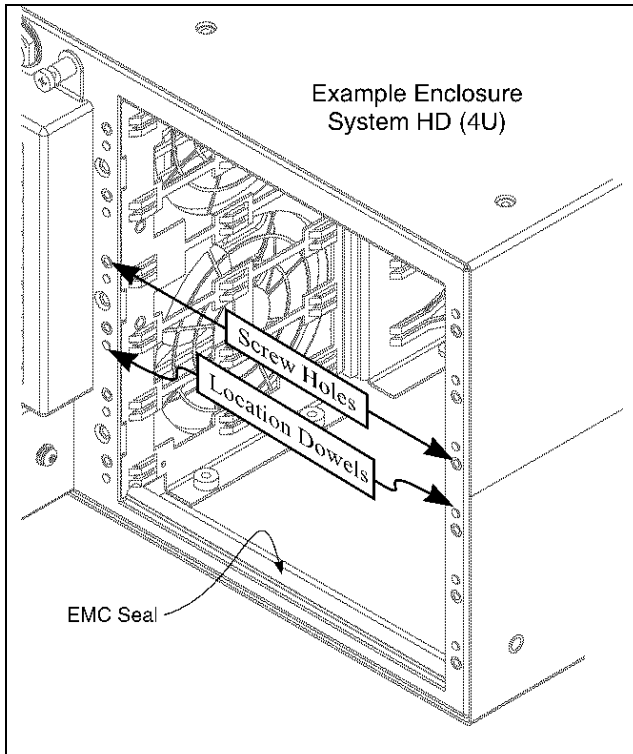
Gently push the rear interface into the enclosure taking care to keep it as straight as possible.

Ensure that neither the EMC seal on the interface below or the EMC seal on the rear interface being located, are damaged as the interface is pushed fully home.

When the flanges on either side of the rear interface are touching the main chassis of the enclosure the interface is fully inserted.

There are two captive retaining screws on the rear interface, one on either side. Adjacent to these screws are two small holes, again one on each side. These holes are designed to aid the precise location of the interface so that the screws can be tightened easily.

A diagram of the rear of an example enclosure is shown on the following page.



Gently move the rear interface against the rear of the enclosure until the small dowels on the chassis locate in the matching holes on the rear interface. The two retaining screws can then be tightened.

The rear interface is now located and secured.

If desired the connections to the rear interface can be made at this stage, if desired. The connector and signal allocations are described later in this document.

Installation of the Main Board

Installation of the main board part of the System HD module is accomplished from the front of the enclosure.

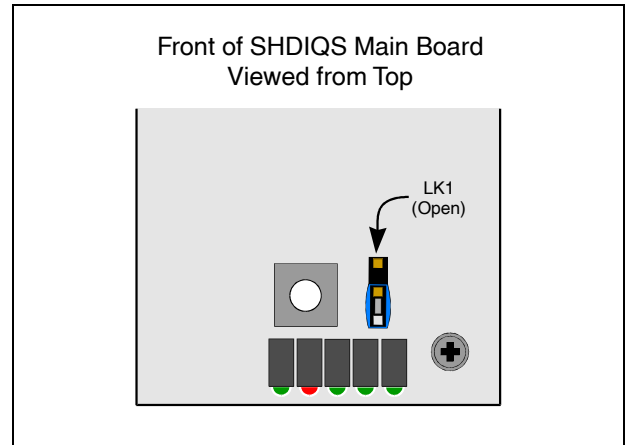
The front panel of the enclosure must be opened. Instructions for this process can be found in the Installation section of the manual for the specific enclosure within which the module is to be installed.

Selecting the Power Enable Mechanism

There are two options for controlling the power control sequencer on the System HD Module. The selection between these two options is made using a simple two pin link on the main board part of the module.

The criterion for choosing which option to use is the presence, or not, of a System HD Control and Monitor board within the enclosure into which the module is being placed.

The position of this link on the main board is shown in the following diagram.



Option 1 : Installation in an Enclosure with a Control and Monitor Board fitted.

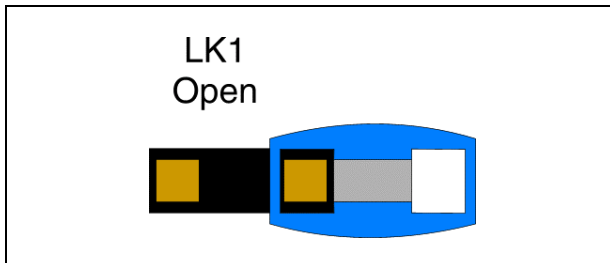
This option describes the selection required for an enclosure within which it is intended that a System HD Control and Monitor board is going to be, or is already, installed.

In this case it will be the Control and Monitor board in the enclosure that controls the power sequencer on the module.

The only parameters which can affect this module are the number of power supplies fitted and operational, and the priority level of the slot within which it is placed. These parameters are fully described in the Installation and Operators manuals for the System HD Control and Monitor board.

The link concerned is labelled as **LK1** and will be found very close to the RESET Button on the front edge of the board as shown in the diagram above.

To ensure that the normal mechanisms are operating it is necessary to confirm that the link is configured as OPEN i.e. with the two pins unconnected (normal power control mechanisms not overridden), which is how the link should be configured when the board is delivered from the factory. See diagram below:



The “bridge” used to make the connection should just be pushed onto one of the pins. This is the correct configuration for a system with a Control and Monitor board fitted. This link configuration allows the APMS (Automatic Power Management System) on the Control and Monitor board to include this module in the management process.

Option 2 : Installation in an Enclosure *without* a Control and Monitor Board.

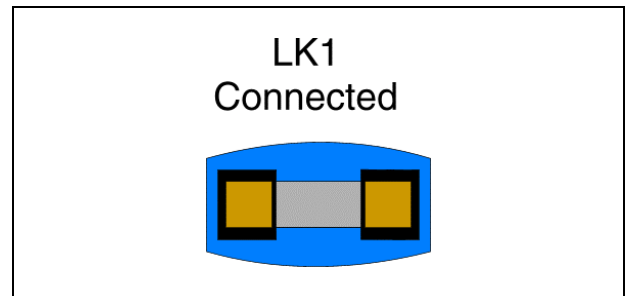
This option describes the required selection for an enclosure where it is not intended that a System HD Control and Monitor board is going to be installed.

In this case it is obviously impossible for the APMS provided by the Control and Monitor board to operate. Thus it is necessary to override the power enable control that would normally be managed by the Control and Monitor board. This is achieved using the two pin link denoted LK1.

This link will be found very close to the RESET Button on the front edge of the board as shown in the diagram on the preceding page.

When delivered from the factory the two pins of this link will not be connected together. The “bridge” used to make the connection should just be pushed onto one of the pins as shown in the diagram in the preceding section. This is NOT the correct setting for an enclosure without a Control and Monitor board

The correct setting for an enclosure without a Control and Monitor board is with the two pins of the link connected together. This configuration is shown in the diagram below.



Pull the “bridge” off of the pin and push it back onto the link (LK1) so that it forms a connection between the two pins.

Inserting the Module Main Board

Final Checks – What to expect

There are two principle conditions related to the enclosure into which the module is about to be fitted which affect what happens as the main board is inserted. These are :

- 1) Power status of the enclosure (On/Off)
- 2) Control and Monitor Board (Fitted/Not Fitted)

Obviously, if the enclosure is not powered up, then the module will not power up as it is inserted.

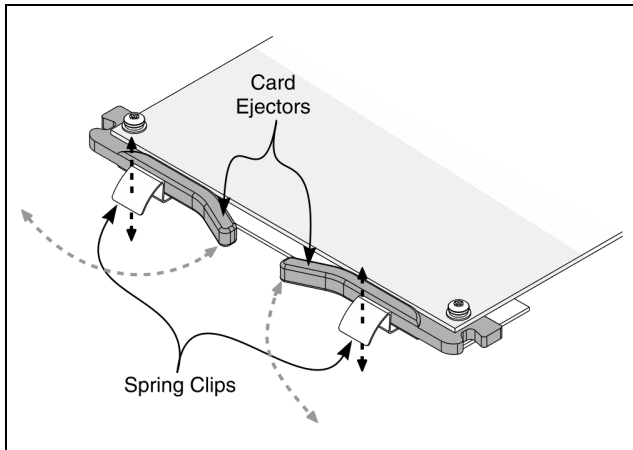
If the enclosure is powered up and there is NO Control and Monitor board fitted then the power enable link (LK1) should have been fitted as described in the previous section. In this case the board will power up as soon as it is inserted – as long as there is sufficient power for it to operate. The availability of sufficient power must have been checked before any attempt is made to insert an additional module. The calculations to enable this check to be made are fully described in the installation manual for the Enclosure.

This manual will assume that this check has taken place and that there is sufficient power available.

If the enclosure is powered up and there IS a Control and Monitor board fitted then the power for the additional module will be managed by the APMS. This process is fully described in the installation manual for the Control and Monitor board. It is important that the description of the APMS is read and understood before a module is installed in an enclosure incorporating a Control and Monitor board.

Fitting the Module Main Board

The main board should now be pushed into the card guides for the slot which has been selected. Before it has been pushed all the way into the enclosure the ejector handles on the front edge of the board should be released from their spring clips. The front edge of the board is illustrated below.



To release the ejector handles from their locked position the spring clips must be pushed down as the handle is drawn back.

To aid the insertion of the main board the handles must be drawn back to slightly less than a right angle to the main board front edge.

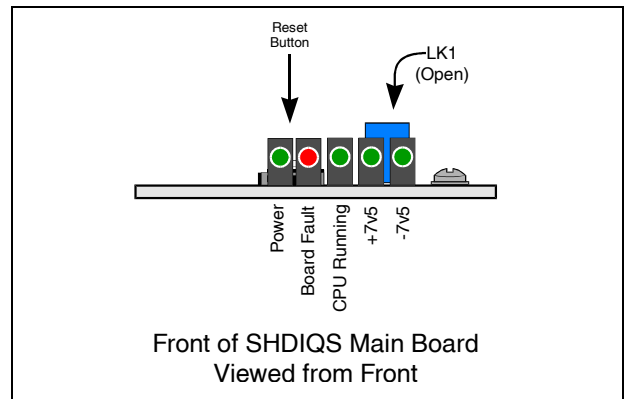
When the board is then pushed into the enclosure it should meet resistance just as the ejector handles are in position to pick up on the chassis flanges on either side of the board.

Pushing the ejector handles towards the front edge of the board will engage them behind the flanges. Then, as they are pushed all the way in, they will fully insert the main board and snap into their locked position.

The main board is then fully installed. Depending on the factors described earlier in this document it may power up immediately. Consult the LED descriptions later in this document for the meanings of the indications provided.

The system connections to the rear panel can now be made if they are not already in place.

Monitoring Indications



There are five LED indicators mounted on the front of SHDIQS main board.

Power

This Green LED will be illuminated if the board is receiving the correct power supplies.

Board Fault

This Red LED will be illuminated if a fault is detected on the board.

CPU Running

This Green LED will flash at a regular rate to indicate that the CPU is running.

+7v5 and -7v5

These Green LED's will be illuminated if the associated power supplies are present.

Removal of the Module

It is not normally necessary to power down the enclosure before removing either the main board section of a System HD Module or it's associated rear interface.

However it is very important to note that there may be factors to do with the location and other operating conditions of the enclosure which preclude the safe live removal of modules.

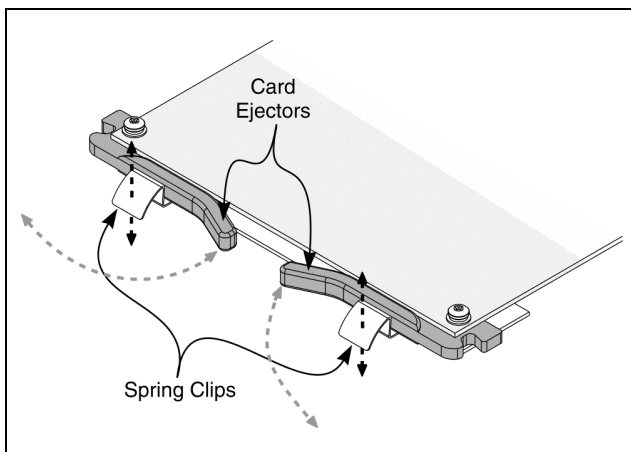
Removal of the Main Board

It is not necessary for any of the rear cabling associated with the module to be removed to facilitate the extraction of the main board part of the module.

Removal of the main board section of a System HD Module is a simple reversal of the method used to insert it.

The removal of the main board section of the module will require access behind the front panel of the enclosure. The appropriate section of the installation manual for the enclosure should be consulted before opening the front panel.

To remove the main board section of the module the two retaining clips must be pressed down to allow the ejector handles to swing away from the main board. See the diagram below.



The action of rotating the ejector handles away from the main board will automatically start to draw the board out of the enclosure. The movement using the ejector handles is sufficient to disconnect the main board from the Services Backplane and it's associated rear interface.

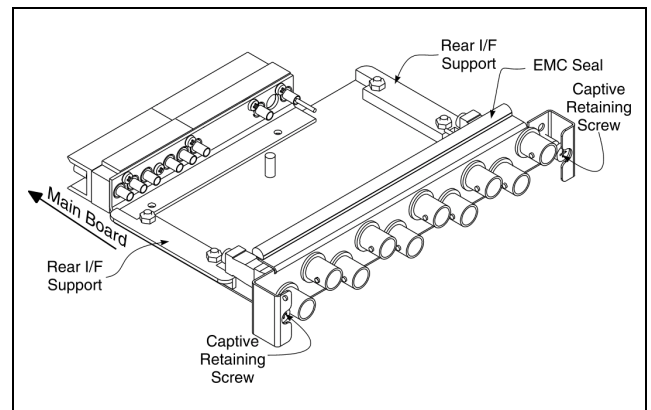
The main board can now be removed from the enclosure.

Removal of the Rear Interface

It is obviously necessary to disconnect all of the rear cabling to a module before it's rear interface can be removed.

The main board section of the module must also be detached from the rear interface even if it is not fully removed from the enclosure.

Undo the captive retaining screws on each side of the rear interface.



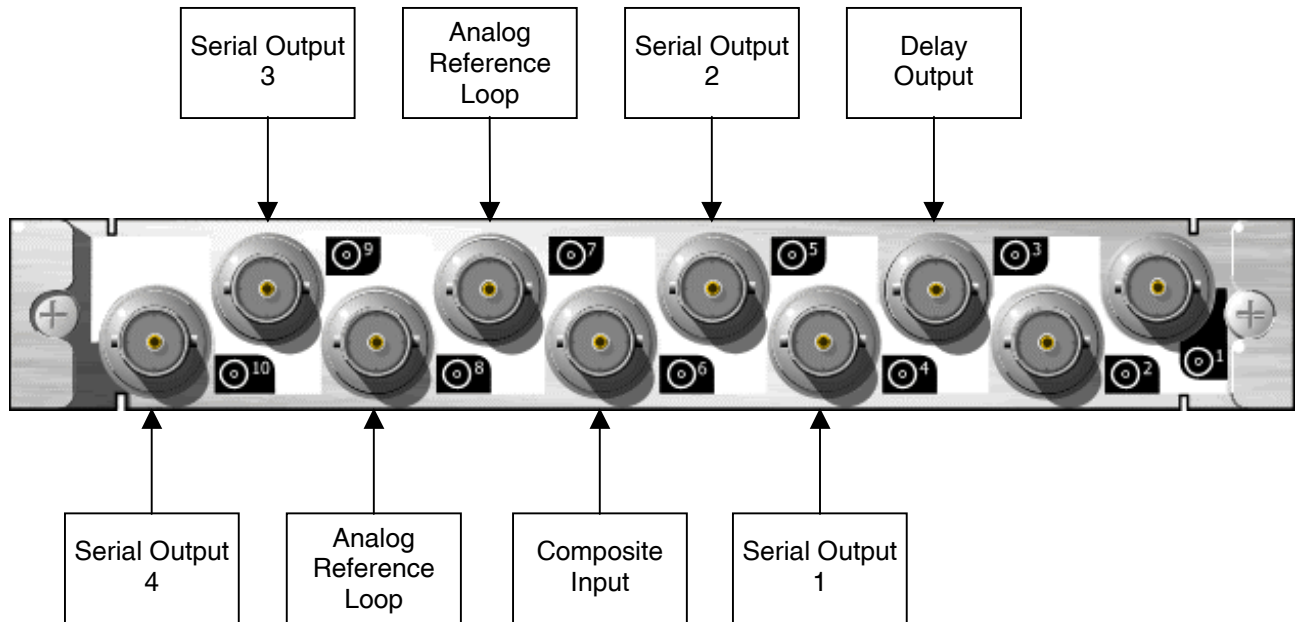
Hold the flanges on either side and gently rock the interface out of the enclosure.

Keep it as straight as possible as it comes out and take care not to damage the EMC Seal on this rear interface or the one below.

Rear Interface Connections

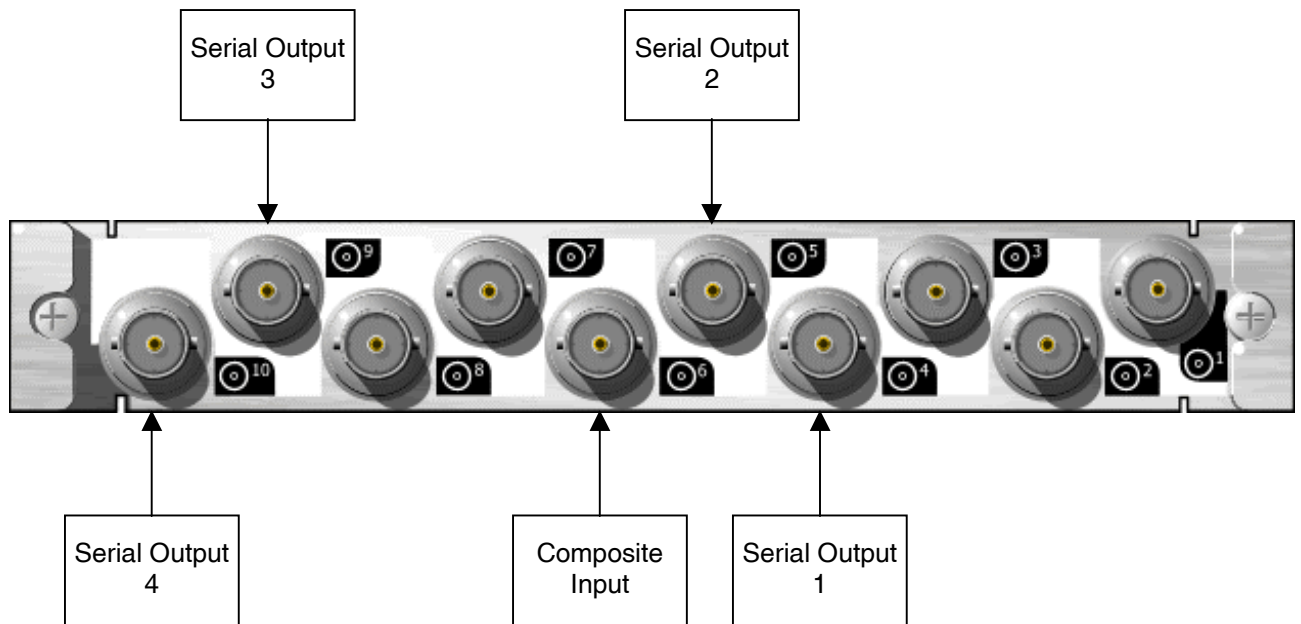
DECODERS

SDMDS-2-0-0 Multi-Standard Decoder + Synchroniser



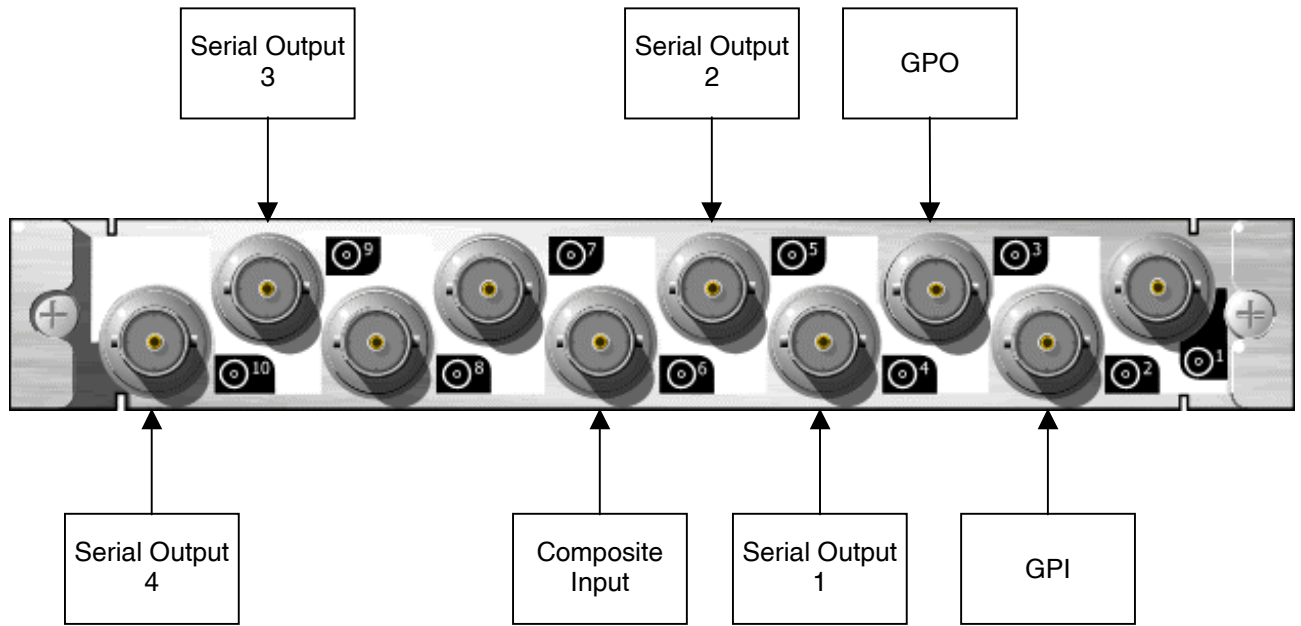
For operating information please refer to the IQDMSDS Operation Manual

SDMSDD-2-0-0 Multi-Standard Decoder



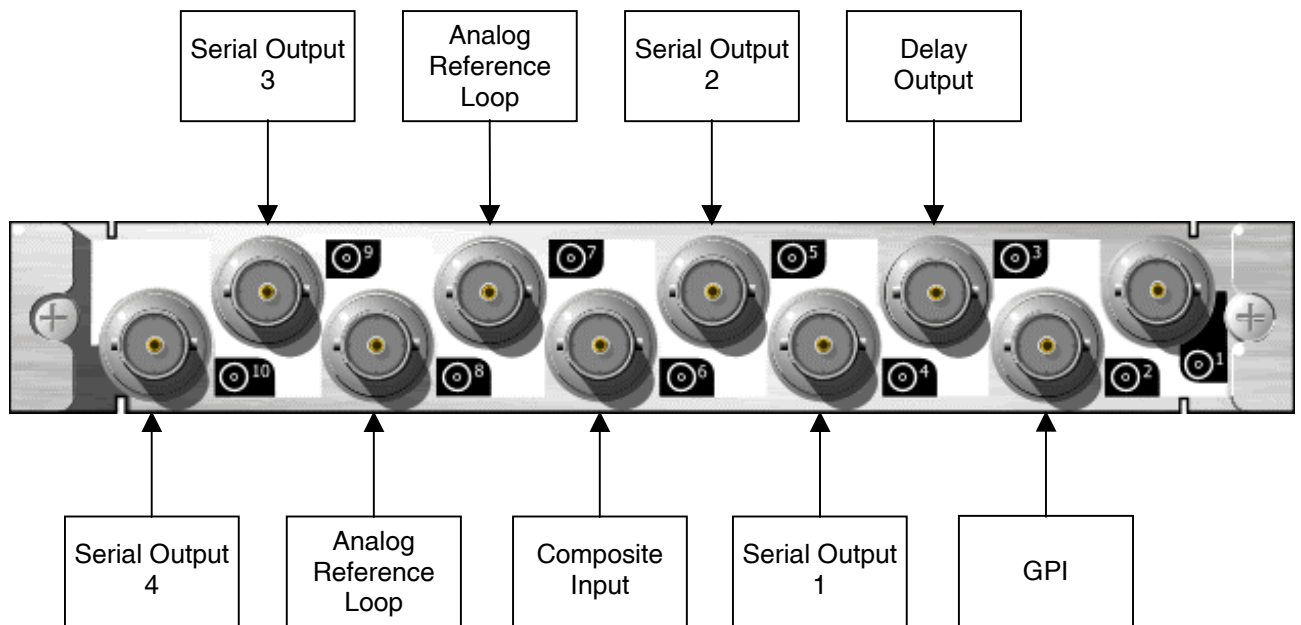
For operating information please refer to the IQDMSDD Operation Manual

SDAMDD-2 Advanced Multi-Standard Decoder



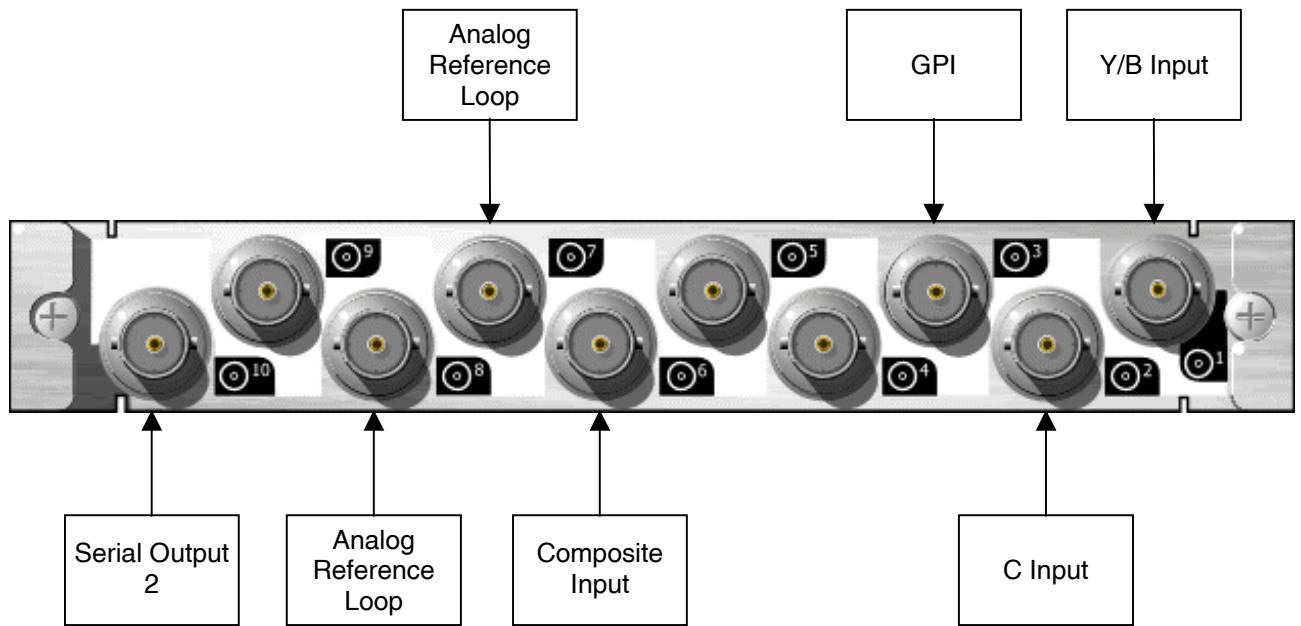
For operating information please refer to the IQDAMDD Operation Manual

SDAMDA-2 Advanced Multi-Standard Decoder + Synchroniser



For operating information please refer to the IQDAMDA Operation Manual

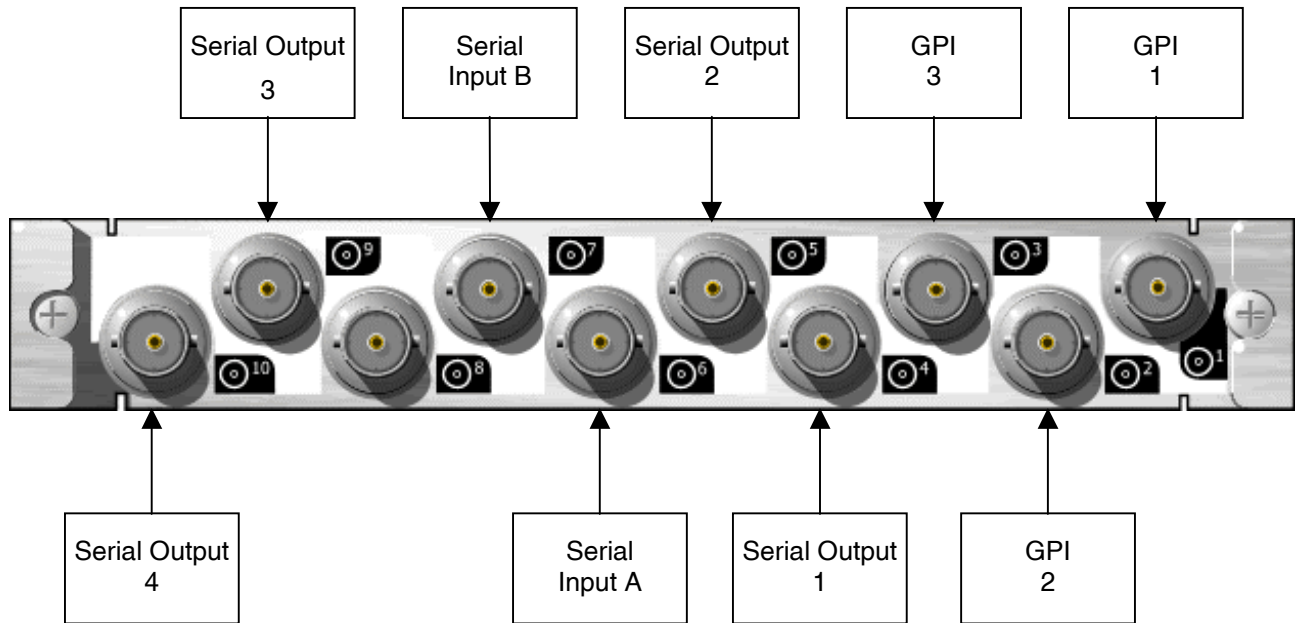
SDMSDL-2 Multi-Standard Decoder + Synchroniser + Noise Reduction



For operating information please refer to the IQDMSDL Operation Manual

NOISE REDUCERS

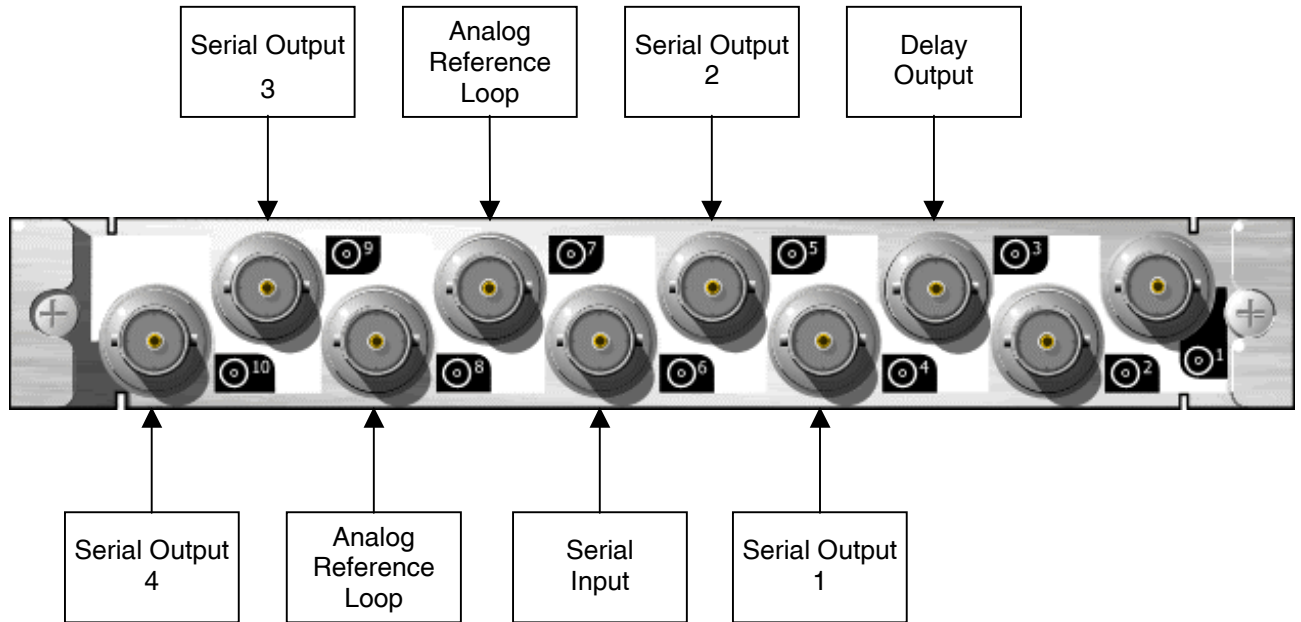
SDANR-2 Advanced Noise Reducer



For operating information please refer to the IQDANR Operation Manual

FRAME SYNCHRONISERS

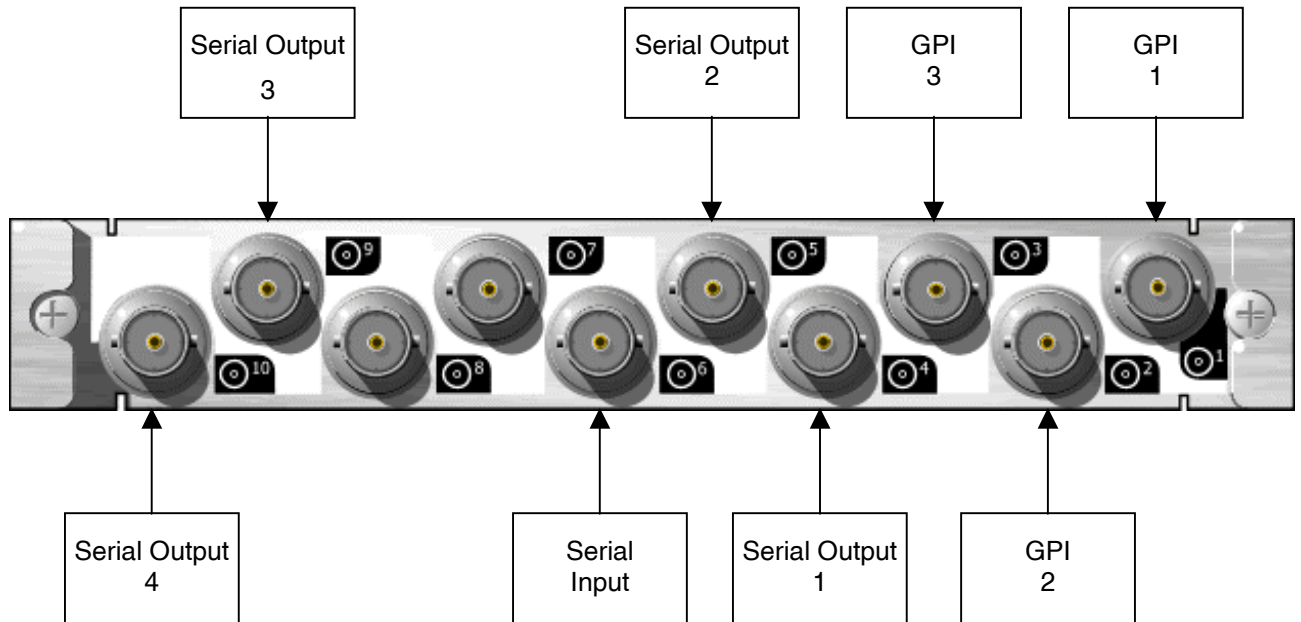
SD1FSY-2-D-E Frame Synchroniser
SD1FSY-2-0E



For operating information please refer to the IQD1FSY Operation Manual

LOGO INSERTERS

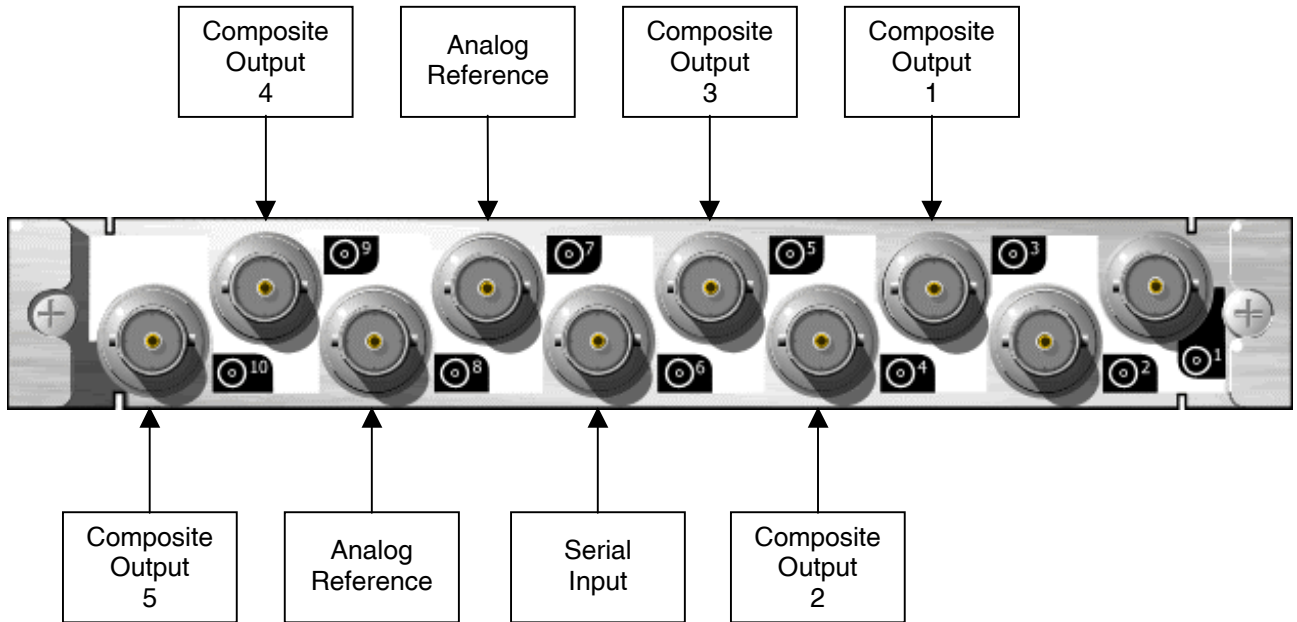
SDLOG-2 Logo Inserter



For operating information please refer to the IQDLOG Operation Manual

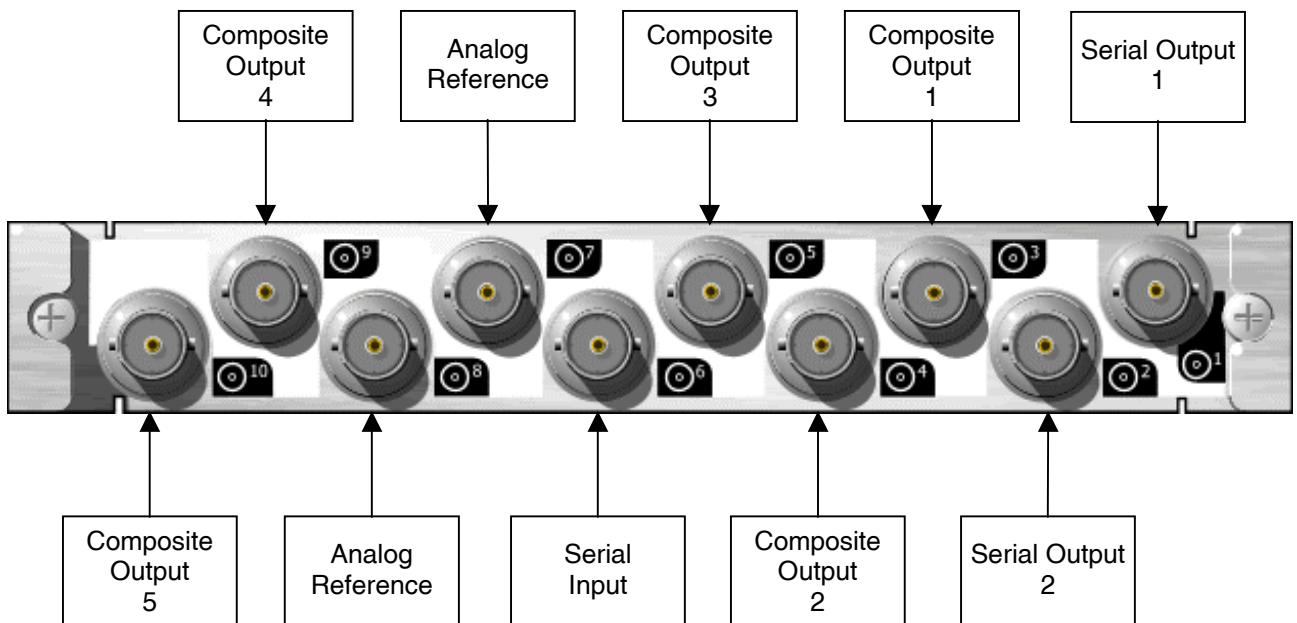
ENCODERS

SDMENCS-2 Encoder (Note: There will be only 5 Outputs)



For operating information please refer to the IQDMENCS Operation Manual

SDMSES-2 Encoder (Note: There will be only 5 Outputs)



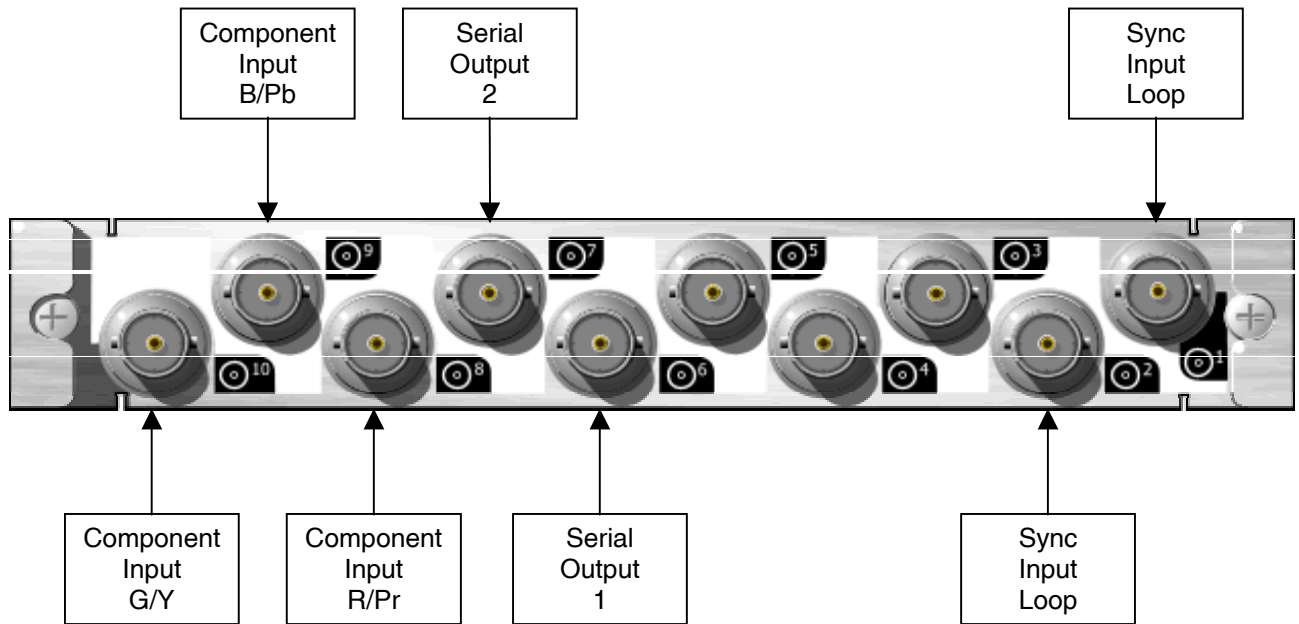
For operating information please refer to the IQDMENCS Operation Manual

ADC'S

SD1ADC-210-B Analogue to Digital Converters

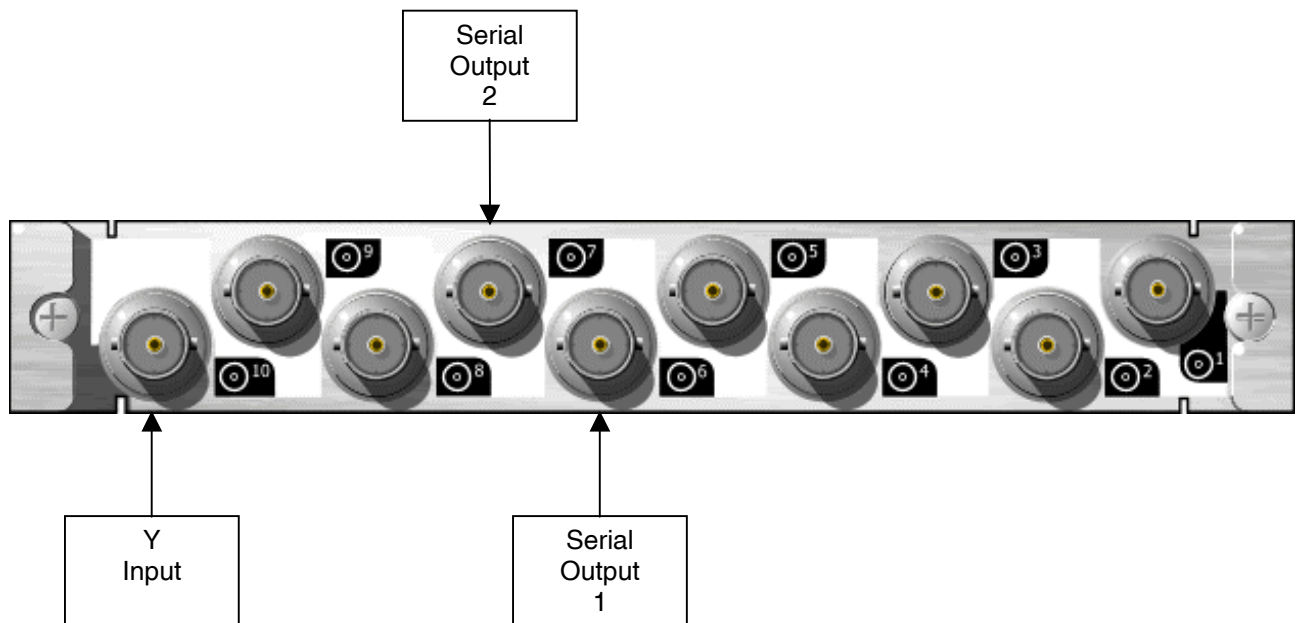
SD1ADC-210-E

SD1ADC-210FO



For operating information please refer to the IQD1ADC Operation Manual

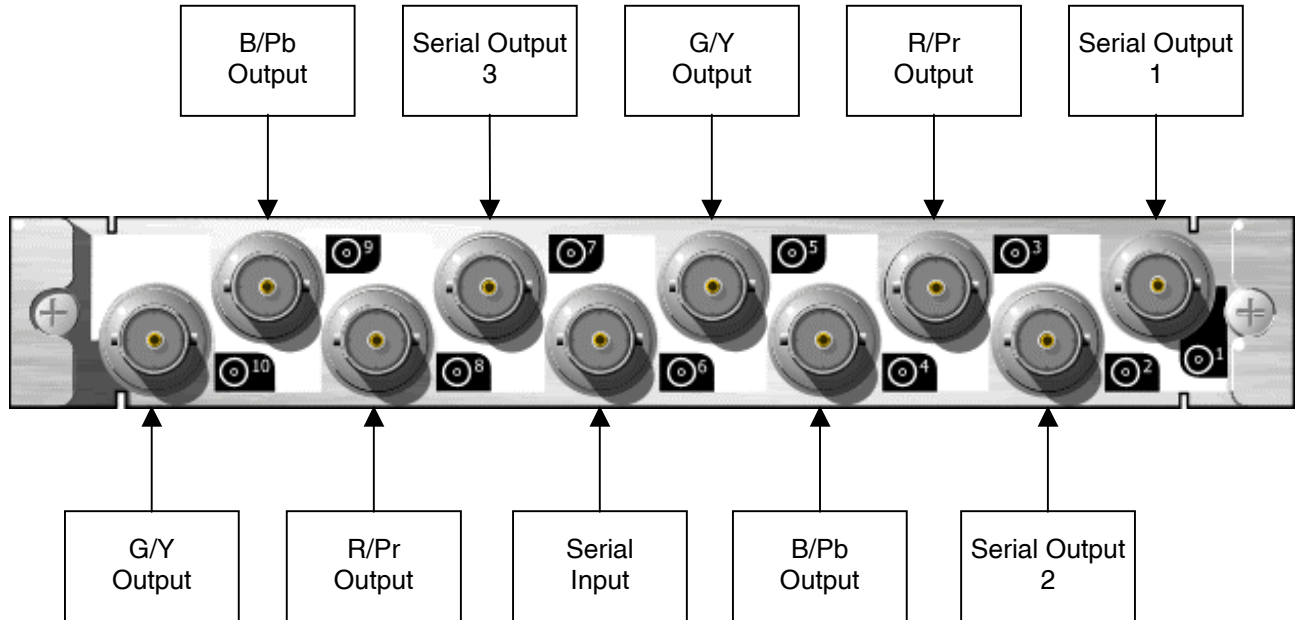
SD1ADC-210-K Analogue to Digital Converter



For operating information please refer to the IQD1ADC Operation Manual

DAC's

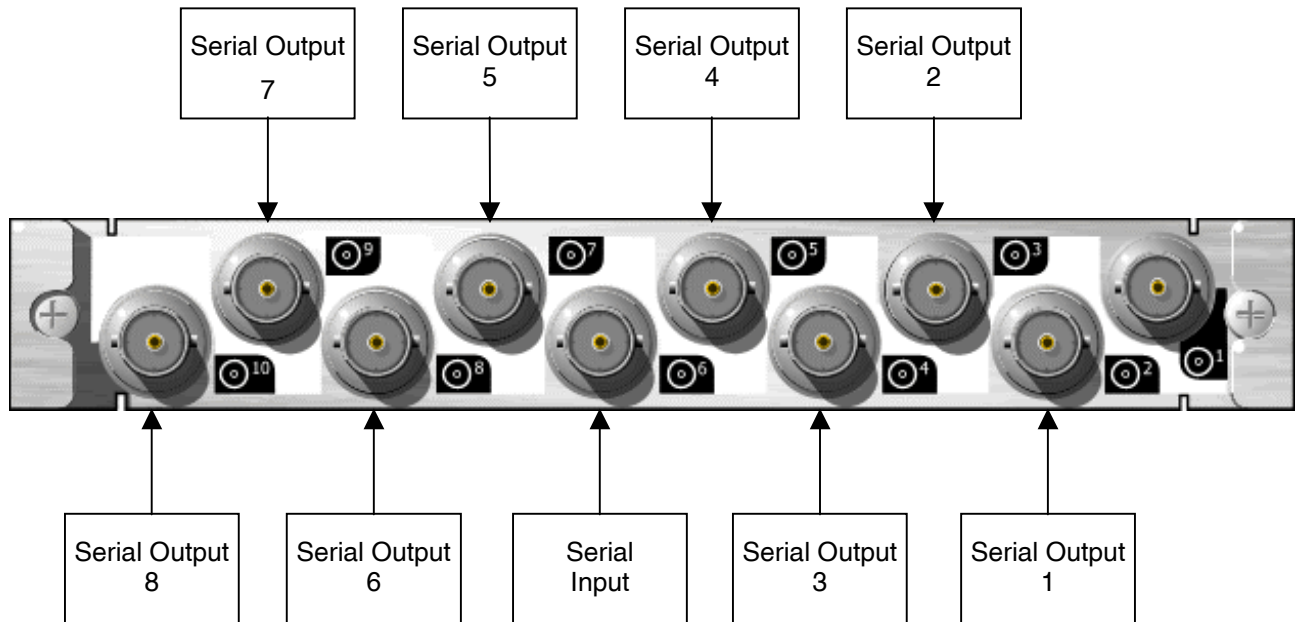
SDDAC-2 Video Digital to Analog Converter



For operating information please refer to the IQDDAC Operation Manual

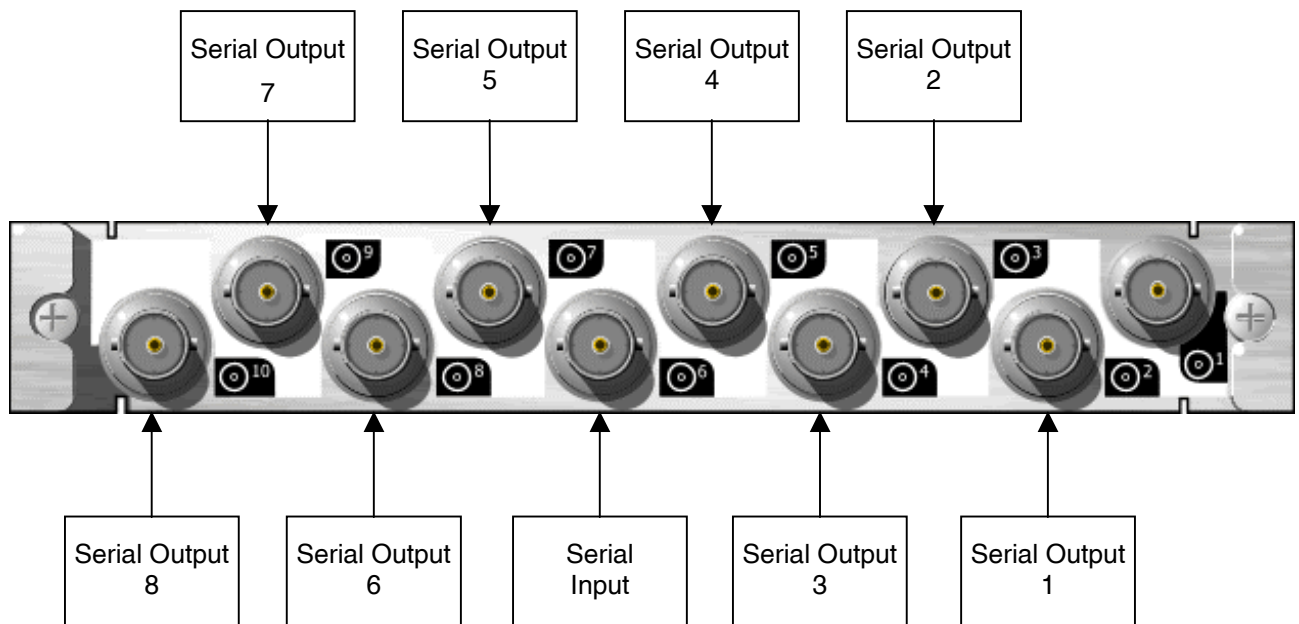
DA's

SDSDR-2-0 SDI Dual Format Reclocking Distribution Amplifier



For operating information please refer to the IQDSDR Operation Manual

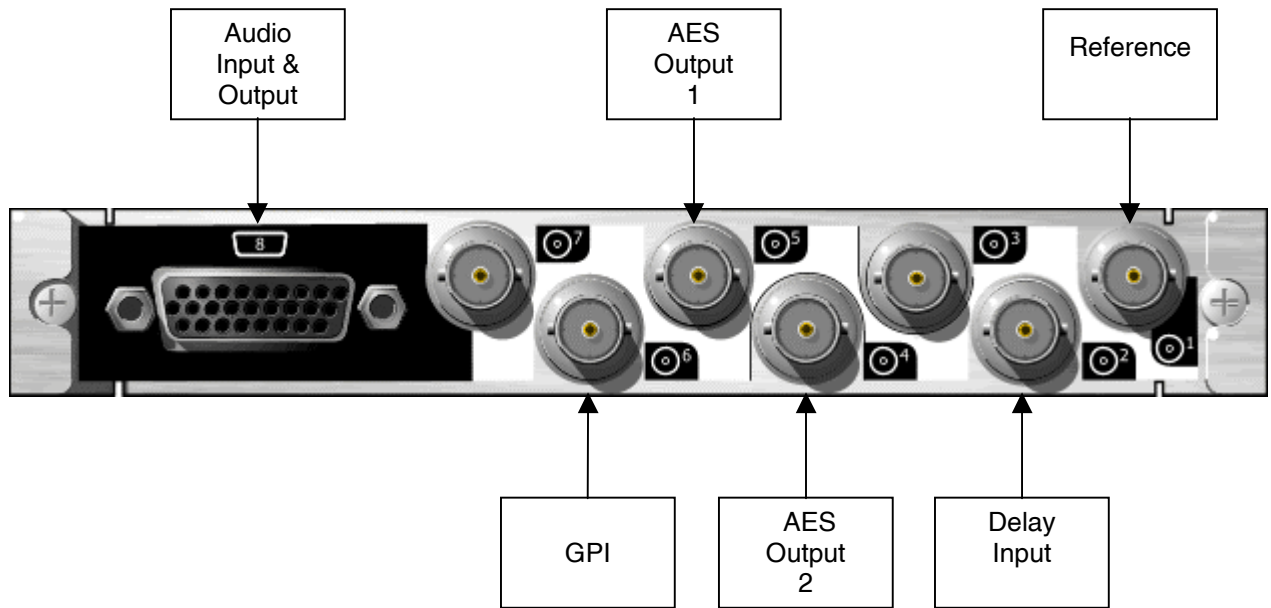
SDSDR-2-R SDI Dual Format Reclocking Distribution Amplifier



For operating information please refer to the IQDSDR Operation Manual

AUDIO

SDBADCD-2 4 Channel Audio ADC + Delay

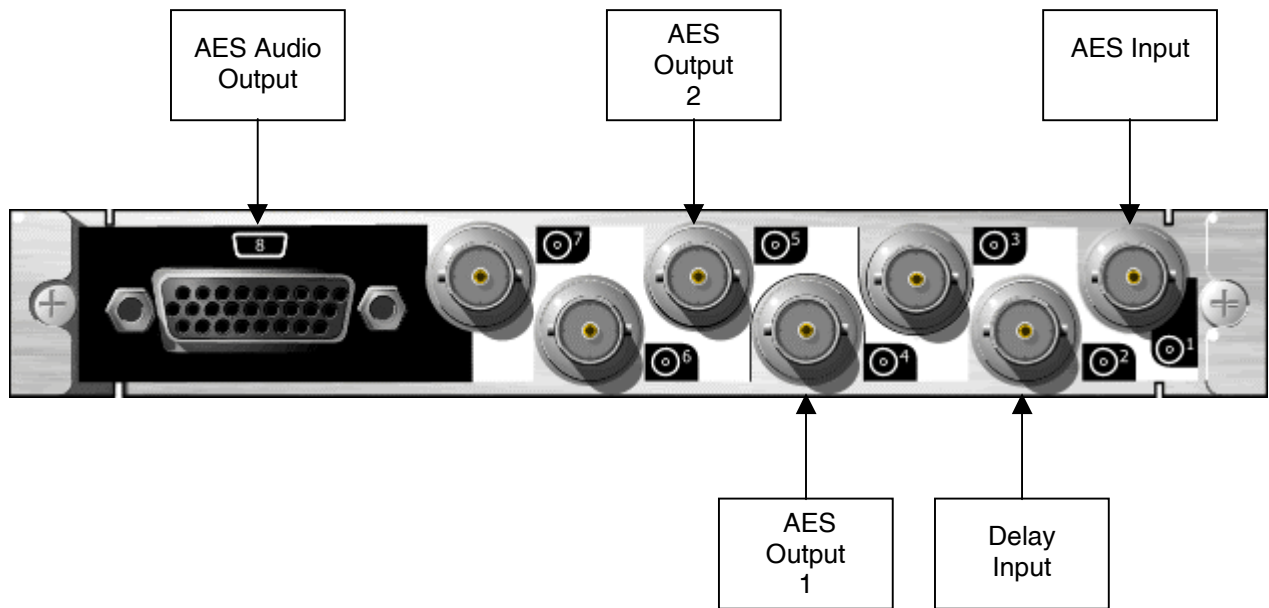


For operating information please refer to the IQBADCD Operation Manual

Connection Details SBADCD (please see page 30 for diagram of standard connections)

| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|-----------------|-----------------------------|-------------------------|-------------|
| 20 | ANALOG AUDIO IN 1 LEFT GND | GND1 | 1 |
| 11 | ANALOG AUDIO IN 1+ LEFT | Ch 1+ | 2 |
| 1 | ANALOG AUDIO IN 1- LEFT | Ch 1- | 3 |
| 21 | ANALOG AUDIO IN 1+RIGHT | Ch 2+ | 2 |
| 12 | ANALOG AUDIO IN 1- RIGHT | Ch 2- | 3 |
| 2 | ANALOG AUDIO IN 1 RIGHT GND | GND2 | 1 |
| 22 | ANALOG AUDIO IN 2 LEFT GND | GND3 | 1 |
| 13 | ANALOG AUDIO IN 2+ LEFT | Ch 3+ | 2 |
| 3 | ANALOG AUDIO IN 2- LEFT | Ch 3- | 3 |
| 23 | ANALOG AUDIO IN 2+ RIGHT | Ch 4+ | 2 |
| 14 | ANALOG AUDIO IN 2- RIGHT | Ch 4- | 3 |
| 4 | ANALOG AUDIO IN 2 RIGHT GND | GND4 | 1 |
| 24 | | GND5 | 1 |
| 15 | | Ch 5+ | 2 |
| 5 | | Ch 5- | 3 |
| 25 | AES AUDIO OUT 1 + | Ch 6+ | 2 |
| 16 | AES AUDIO OUT 1 - | Ch 6- | 3 |
| 6 | AES AUDIO OUT 1 GND | GND6 | 1 |
| 26 | AES AUDIO OUT 2 GND | GND7 | 1 |
| 17 | AES AUDIO OUT 2 + | Ch 7+ | 2 |
| 7 | AES AUDIO OUT 2 - | Ch 7- | 3 |
| 9 | AES AUDIO REF IN + | Ch 8+ | 2 |
| 18 | AES AUDIO REF IN - | Ch 8- | 3 |
| 8 | AES AUDIO REF GND | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SDBDAD-2-B 2 Channel Digital Audio Delay

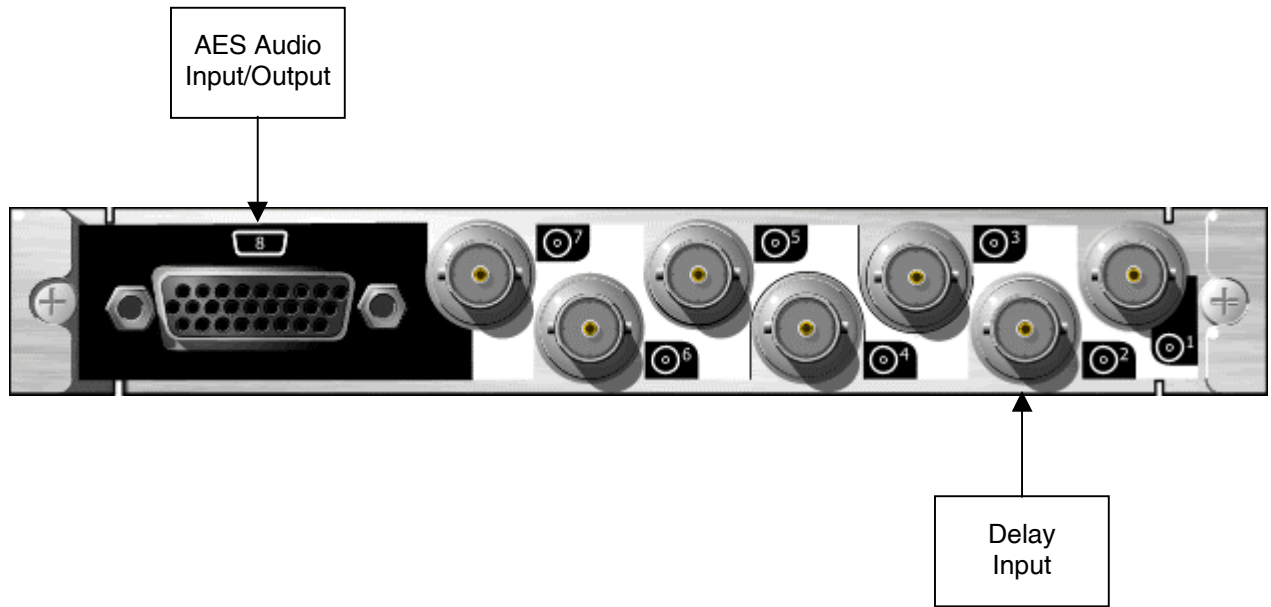


For operating information please refer to the IQBDAD Operation Manual

Connection Details SDBDAD (please see page 30 for diagram of standard connections)

| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|-----------------|---------------|-------------------------|-------------|
| 20 | AES OUT 1 GND | GND1 | 1 |
| 11 | AES OUT 1 + | Ch 1+ | 2 |
| 1 | AES OUT 1 - | Ch 1- | 3 |
| 21 | AES OUT 2 + | Ch 2+ | 2 |
| 12 | AES OUT 2 - | Ch 2- | 3 |
| 2 | AES OUT 2 GND | GND2 | 1 |
| 22 | | GND3 | 1 |
| 13 | | Ch 3+ | 2 |
| 3 | | Ch 3- | 3 |
| 23 | | Ch 4+ | 2 |
| 14 | | Ch 4- | 3 |
| 4 | | GND4 | 1 |
| 24 | | GND5 | 1 |
| 15 | | Ch 5+ | 2 |
| 5 | | Ch 5- | 3 |
| 25 | | Ch 6+ | 2 |
| 16 | | Ch 6- | 3 |
| 6 | | GND6 | 1 |
| 26 | | GND7 | 1 |
| 17 | | Ch 7+ | 2 |
| 7 | | Ch 7- | 3 |
| 9 | | Ch 8+ | 2 |
| 18 | | Ch 8- | 3 |
| 8 | | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SDBDAD-1-D 2 Channel Digital Audio Delay

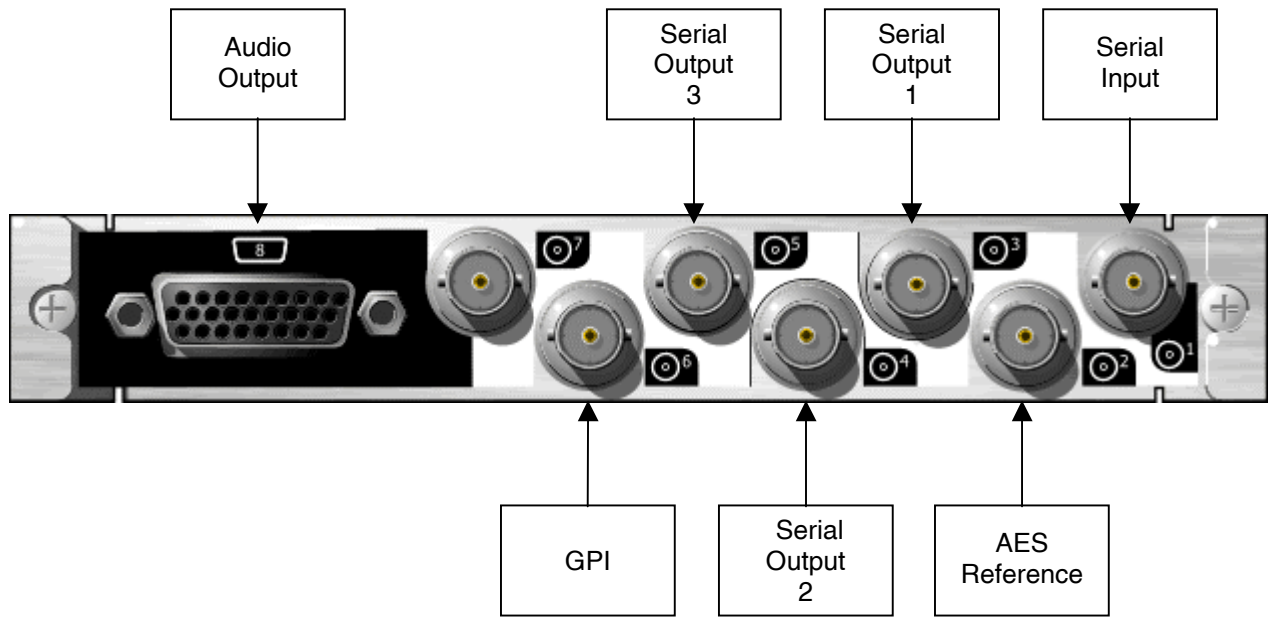


For operating information please refer to the IQBDAD Operation Manual

Connection Details SDBDAD (please see page 30 for diagram of standard connections)

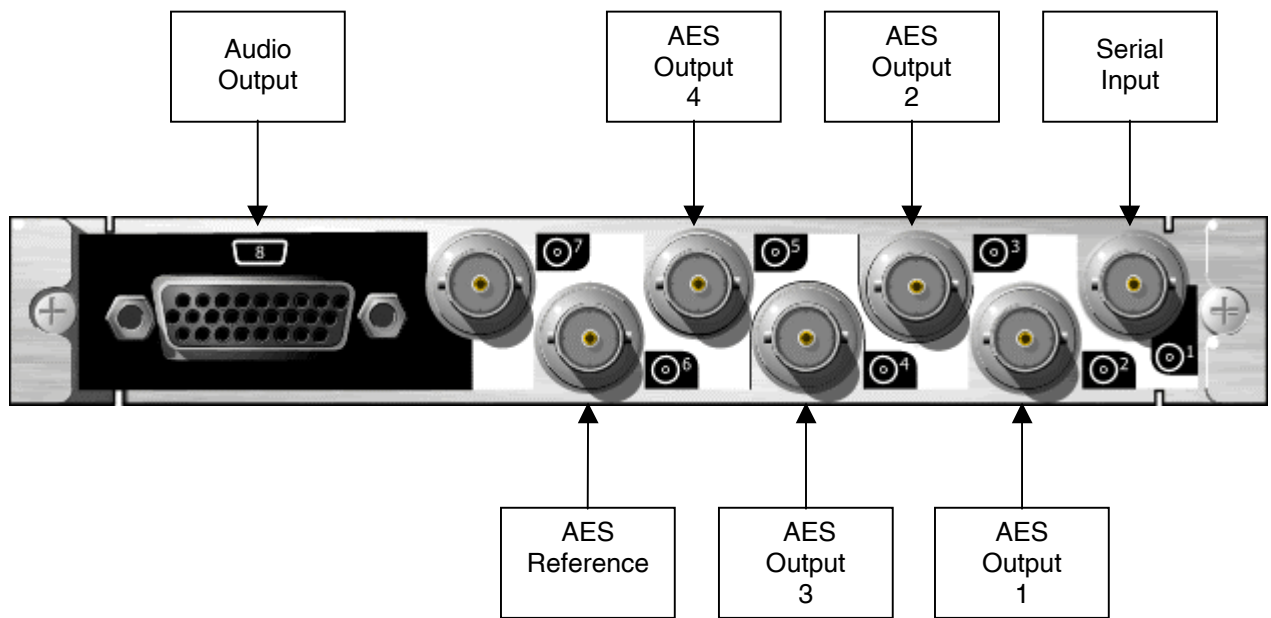
| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|-----------------|---------------|-------------------------|-------------|
| 20 | AES OUT 1 GND | GND1 | 1 |
| 11 | AES OUT 1 + | Ch 1+ | 2 |
| 1 | AES OUT 1 - | Ch 1- | 3 |
| 21 | AES OUT 2 + | Ch 2+ | 2 |
| 12 | AES OUT 2 - | Ch 2- | 3 |
| 2 | AES OUT 2 GND | GND2 | 1 |
| 22 | | GND3 | 1 |
| 13 | | Ch 3+ | 2 |
| 3 | | Ch 3- | 3 |
| 23 | AES OUT 3 + | Ch 4+ | 2 |
| 14 | AES OUT 3 - | Ch 4- | 3 |
| 4 | AES OUT 3 GND | GND4 | 1 |
| 24 | AES OUT 4 GND | GND5 | 1 |
| 15 | AES OUT 4 + | Ch 5+ | 2 |
| 5 | AES OUT 4 - | Ch 5- | 3 |
| 25 | | Ch 6+ | 2 |
| 16 | | Ch 6- | 3 |
| 6 | | GND6 | 1 |
| 26 | AES IN GND | GND7 | 1 |
| 17 | AES IN+ | Ch 7+ | 2 |
| 7 | AES IN - | Ch 7- | 3 |
| 9 | | Ch 8+ | 2 |
| 18 | | Ch 8- | 3 |
| 8 | | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SDBADX-2-D Universal De-Embedding Engine



For operating information please refer to the IQBADX Operation Manual

SDBADX-2-B Universal De-Embedding Engine

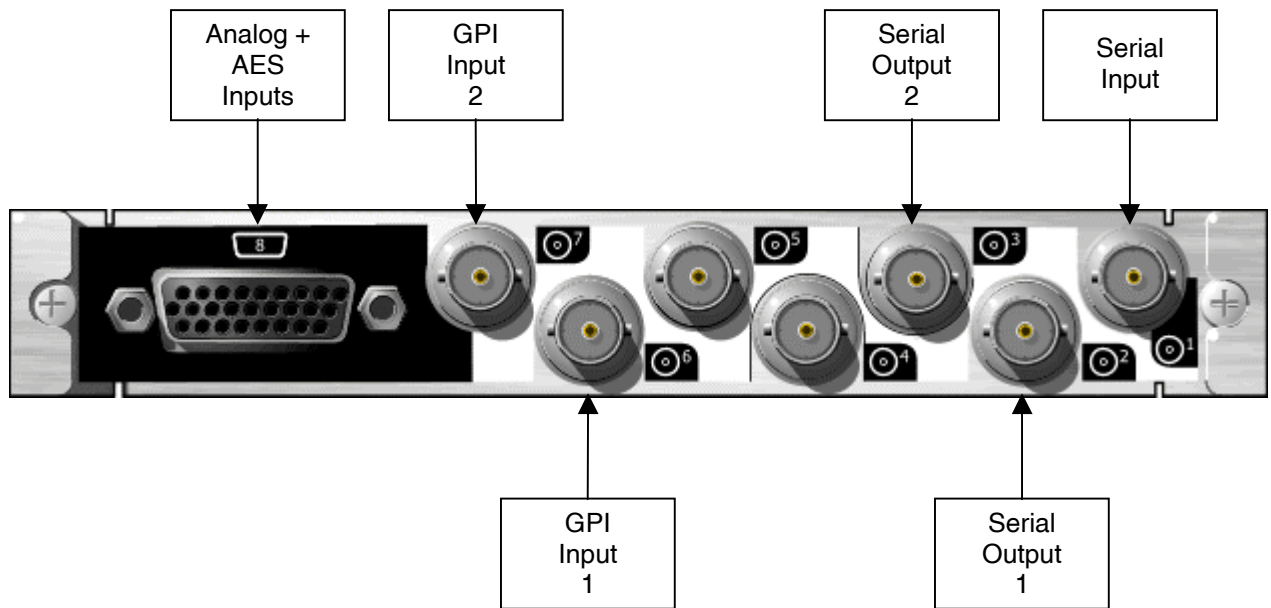


For operating information please refer to the IQBADX Operation Manual

Connection Details IQBADX-D (please see page 30 for diagram of standard connections)

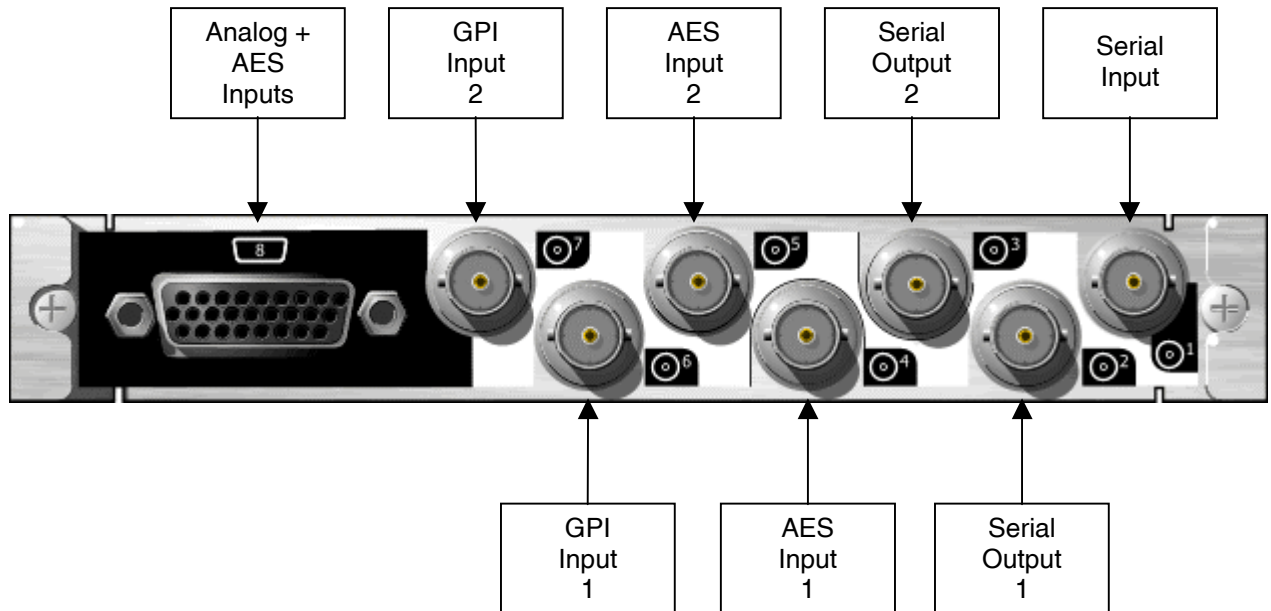
| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|--------------------|------------------------|-------------------------|----------------|
| 20 | ANALOG OUT 1 GND | GND1 | 1 |
| 11 | ANALOG OUT 1 + (Left) | Ch 1+ | 2 |
| 1 | ANALOG OUT 1 - (Left) | Ch 1- | 3 |
| 21 | ANALOG OUT 1 + (Right) | Ch 2+ | 2 |
| 12 | ANALOG OUT 1 - (Right) | Ch 2- | 3 |
| 2 | ANALOG OUT 1 GND | GND2 | 1 |
| 22 | ANALOG OUT 2 GND | GND3 | 1 |
| 13 | ANALOG OUT 2 + (Left) | Ch 3+ | 2 |
| 3 | ANALOG OUT 2 - (Left) | Ch 3- | 3 |
| 23 | ANALOG OUT 2 + (Right) | Ch 4+ | 2 |
| 14 | ANALOG OUT 2 - (Right) | Ch 4- | 3 |
| 4 | ANALOG OUT 2 GND | GND4 | 1 |
| 24 | GND | GND5 | 1 |
| 15 | AES OUT 1 + | Ch 5+ | 2 |
| 5 | AES OUT 1 - | Ch 5- | 3 |
| 25 | AES OUT 2 + | Ch 6+ | 2 |
| 16 | AES OUT 2 - | Ch 6- | 3 |
| 6 | AES OUT 2 GND | GND6 | 1 |
| 26 | AES OUT 3 GND | GND7 | 1 |
| 17 | AES OUT 3 + | Ch 7+ | 2 |
| 7 | AES OUT 3 - | Ch 7- | 3 |
| 9 | AES OUT 4 + | Ch 8+ | 2 |
| 18 | AES OUT 4 - | Ch 8- | 3 |
| 8 | AES OUT 4 GND | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SDBADI-2-D Universal Embedding Engine



For operating information please refer to the IQBADI Operation Manual

SDBADI-2-B Universal Embedding Engine

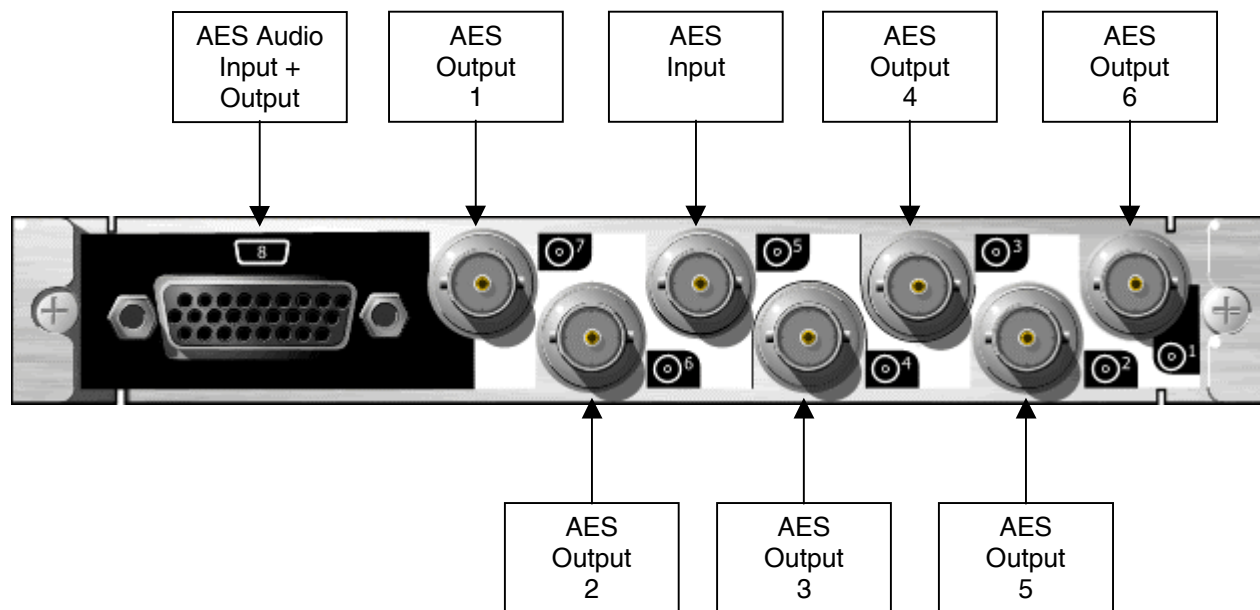


For operating information please refer to the IQBADI Operation Manual

Connection Details SBADI (please see page 30 for diagram of standard connections)

| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|--------------------|-------------------|-------------------------|----------------|
| 20 | | GND1 | 1 |
| 11 | | Ch 1+ | 2 |
| 1 | | Ch 1- | 3 |
| 21 | | Ch 2+ | 2 |
| 12 | | Ch 2- | 3 |
| 2 | | GND2 | 1 |
| 22 | AES IN 1 GND | GND3 | 1 |
| 13 | AES IN 1 + | Ch 3+ | 2 |
| 3 | AES IN 1 - | Ch 3- | 3 |
| 23 | AES IN 2 + | Ch 4+ | 2 |
| 14 | AES IN 2 - | Ch 4- | 3 |
| 4 | AES IN 2 GND | GND4 | 1 |
| 24 | INPUT B LEFT GND | GND5 | 1 |
| 15 | INPUT B LEFT + | Ch 5+ | 2 |
| 5 | INPUT B LEFT - | Ch 5- | 3 |
| 25 | INPUT B RIGHT + | Ch 6+ | 2 |
| 16 | INPUT B RIGHT - | Ch 6- | 3 |
| 6 | INPUT B RIGHT GND | GND6 | 1 |
| 26 | INPUT A LEFT GND | GND7 | 1 |
| 17 | INPUT A LEFT + | Ch 7+ | 2 |
| 7 | INPUT A LEFT - | Ch 7- | 3 |
| 9 | INPUT A RIGHT + | Ch 8+ | 2 |
| 18 | INPUT A RIGHT - | Ch 8- | 3 |
| 8 | INPUT A RIGHT GND | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SBDDAS-2-M-B AES distribution amplifier

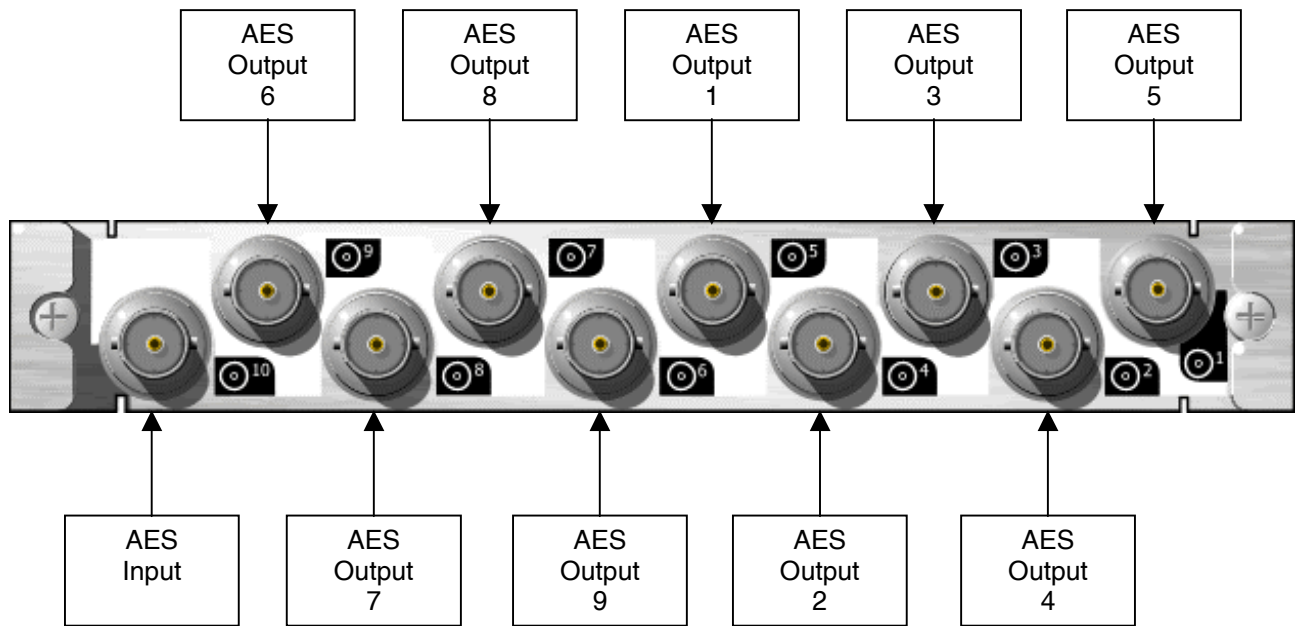


For operating information please refer to the IQBDDAS Operation Manual

Connection Details SBDDAS (please see page 30 for diagram of standard connections)

| 26 Way D Pin No | Description | Standard Pin Assignment | XLR Pin No. |
|-----------------|---------------------|-------------------------|-------------|
| 20 | AES OUT 1 Ground | GND1 | 1 |
| 11 | AES OUT 1 + | Ch 1+ | 2 |
| 1 | AES OUT 1 - | Ch 1- | 3 |
| 21 | AES OUT 2 + | Ch 2+ | 2 |
| 12 | AES OUT 2 - | Ch 2- | 3 |
| 2 | AES OUT 2 Ground | GND2 | 1 |
| 22 | AES OUT 3 Ground | GND3 | 1 |
| 13 | AES OUT 3 + | Ch 3+ | 2 |
| 3 | AES OUT 3 - | Ch 3- | 3 |
| 23 | Analog OUT R + | Ch 4+ | 2 |
| 14 | Analog OUT R - | Ch 4- | 3 |
| 4 | Analog OUT R Ground | GND4 | 1 |
| 24 | Analog OUT L Ground | GND5 | 1 |
| 15 | Analog OUT L + | Ch 5+ | 2 |
| 5 | Analog OUT L - | Ch 5- | 3 |
| 25 | AES OUT 4 + | Ch 6+ | 2 |
| 16 | AES OUT 4 - | Ch 6- | 3 |
| 6 | AES OUT 4 Ground | GND6 | 1 |
| 26 | AES OUT 5 Ground | GND7 | 1 |
| 17 | AES OUT 5 + | Ch 7+ | 2 |
| 7 | AES OUT 5 - | Ch 7- | 3 |
| 9 | AES IN + | Ch 8+ | 2 |
| 18 | AES IN - | Ch 8- | 3 |
| 8 | AES IN Ground | GND8 | 1 |
| 10 | | Chassis | |
| 19 | | Chassis | |

SBDDAS-2-0-B AES distribution amplifier



For operating information please refer to the IQBDDAS Operation Manual

Standard Connections for 26 way High Density D Type to XLR connectors

